

A Novel Method for Commutation Torque Ripple Reduction of Four-Switch, Three-Phase Brushless DC Motor Drive

A. Halvaei-Niasar*, A. Vahedi** and H. Moghbelli***

Abstract: This paper presents an original study on the generated torque ripples of phase commutation in the Four-Switch, Three-Phase Inverter (FSTPI) Brushless DC (BLDC) motor drive which is suitable for low cost applications. Analytic values of torque ripple and commutation duration are obtained for different operation conditions. Moreover, limitation on the speed range operation caused from splitting of the DC-link voltage is shown exactly. Then a novel current control technique is developed to minimize the commutation torque ripple for a wide speed range. The technique proposed here is based on a strategy that the current slopes of the rising and the decaying phases during the commutation intervals can be equalized by proper duty-ratios at commutations. Finally, the validity of the proposed analysis and developed torque ripple reduction technique are verified via simulation.

Keywords: Brushless DC Motor, Commutation Torque Ripple, Four-Switch Inverter.

1 Introduction

Brushless DC motors with trapezoidal back-EMF have been widely used due to their high efficiency, high torque density, low maintenance and ease of control [1]. BLDC motors are fed with rectangular stator current as shown in Fig. 1 and therefore, the developed torque is theoretically constant. However, in practice torque ripple exists due to the stator inductance, cogging effect and motor feeding [2, 3]. Carlson announced relation between the commutation torque ripple and the speed of a six-switch inverter BLDC motor drive [4]. For low cost application, the four-switch, three-phase inverter (FSTPI) employs only four switches, a pair of complementary switches as shown in Fig. 2. A comparison between six and four switch inverters for a three-phase BLDC motor drive have been presented in [5, 6]. Although in the four-switch inverter, generating of 120° conducting current profiles is inherently difficult. Therefore, in order to use the four-switch topology for three-phase BLDC motor, new control

methods should be used. The reported methods due to asymmetrical voltage problem have paid to conduction interval operation and so there aren't any considerable works for commutation intervals.

In this paper, an original study of commutation for FSTPI topology is proposed and its drawbacks are explored analytically. Then, the possibility of reducing the torque ripple due to commutation for a FSTPI brushless dc motor drive is proposed. Finally, a novel torque ripple reduction method is developed to enhance the drive performance it is demonstrated via simulation.

2 Analysis of BLDC Motor Drive Using FSTPI

2.1 Modeling of FSTPI-BLDC Motor Drive

The overall block diagram of a FSTPI-BLDC motor drive is shown in Fig. 2. Motor is connected to a, b legs of inverter and mid point of DC-Bus. Mathematical model of a three-phase BLDC motor is expressed as:

$$\begin{bmatrix} v_{an} \\ v_{bn} \\ v_{cn} \end{bmatrix} = \begin{bmatrix} R & 0 & 0 \\ 0 & R & 0 \\ 0 & 0 & R \end{bmatrix} \times \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} + \begin{bmatrix} L_s - M & 0 & 0 \\ 0 & L_s - M & 0 \\ 0 & 0 & L_s - M \end{bmatrix} \frac{d}{dt} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} + \begin{bmatrix} e_a \\ e_b \\ e_c \end{bmatrix} \quad (1)$$

and,

$$\begin{cases} v_a = v_{an} + v_{no} \\ v_b = v_{bn} + v_{no} \\ v_c = v_{cn} + v_{no} \end{cases} \quad (2)$$

where, v_{xo} , v_{no} , v_{xn} , i_x , and e_{xn} , are the line terminal voltage and motor star point with respect to point o, phase terminal voltage, phase current, phase back-EMF

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voltage, and R , L_s and M are the phase resistance, phase inductance and mutual inductance respectively [7]. Generally a BLDC motor has two operation region; conduction region and commutation region. In the conduction region, two phases always conduct the current. Commutation region is transient region between two conduction modes and therefore, three phases including rising, decaying, and non-commutating phases are all conducting.

2.2 Current Regulation

In a Four-switch BLDC motor drive, current regulation needs more attention rather than six-switch drive. According to Fig. 1, in mode II and V, phases a and b are conducting the current and phase c is silent. However, the back EMF voltage of phase c (e_c) can cause additional and unexpected current, resulting in

current distortion in the phases a, b. Therefore, if i_a and i_b are controlled independently, influence of the voltage e_c can be blocked and therefore, there isn't any current distortion in phase a and b. Fig. 2 shows the configuration of a FSTPI-BLDC motor drive with employing the direct phase current (DPC) control technique that two current sensors are used. In addition to conduction regions, it is possible to extend the DPC control technique to commutation regions. For low speed conditions, this technique can regulate the currents well. Therefore, the current and torque don't contain any ripple more than hysteresis band. However, it has been shown that DPC control technique can not reduce commutation torque ripple at high speed conditions [8]. Therefore, the effective control technique should be developed.

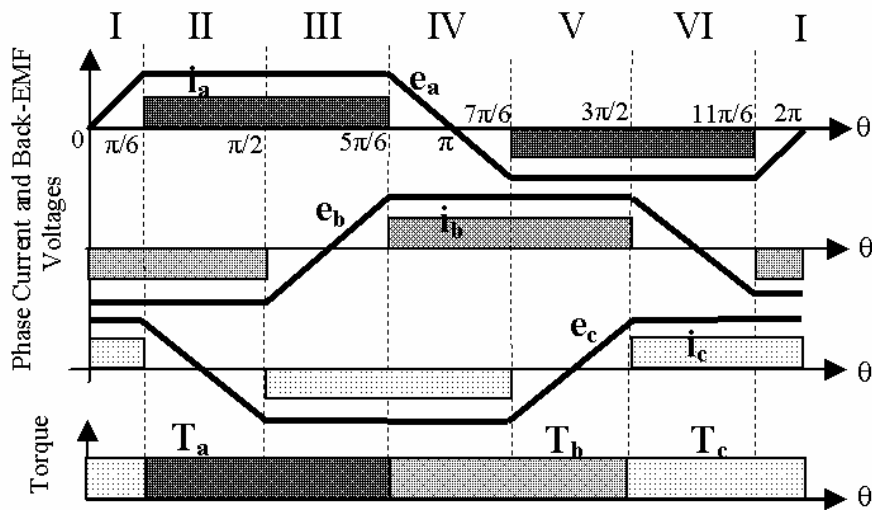


Fig. 1 Ideal signal waveforms of three-phase BLDC motor.

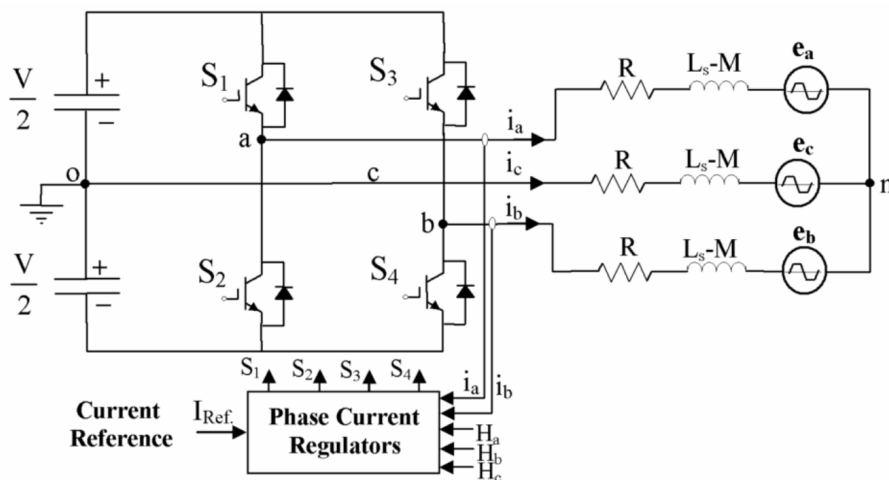


Fig. 2 Configuration of FSTPI-BLDC motor drive system via direct phase current control method.

3 Analysis of Commutation in FSTPI-BLDC Motor

3.1 Phase Current Behaviors in Commutations

Phase commutations in all modes of a six-switch inverter BLDC motor drive are the same together whereas, in four-switch inverter, they are different. Depend on the motor speed, three different cases may be found for each mode as the following:

Case A: Decaying current (i_d) vanishes at the same time that the rising current (i_r) reaches to its final value (+I) as shown in Fig. 3(a).

Case B: Decaying current vanishes before that the rising current reaches to its final value as shown in Fig. 3(b).

Case C: Rising current reaches to (+I) before that the decaying current vanishes as shown in Fig. 3(c).

For the following analysis, the stator resistance is ignored and the phase back-EMF voltages is supposed to remain constant during commutation. These assumptions lead to analytic equations for developed torque ripple values, commutation durations and etc.

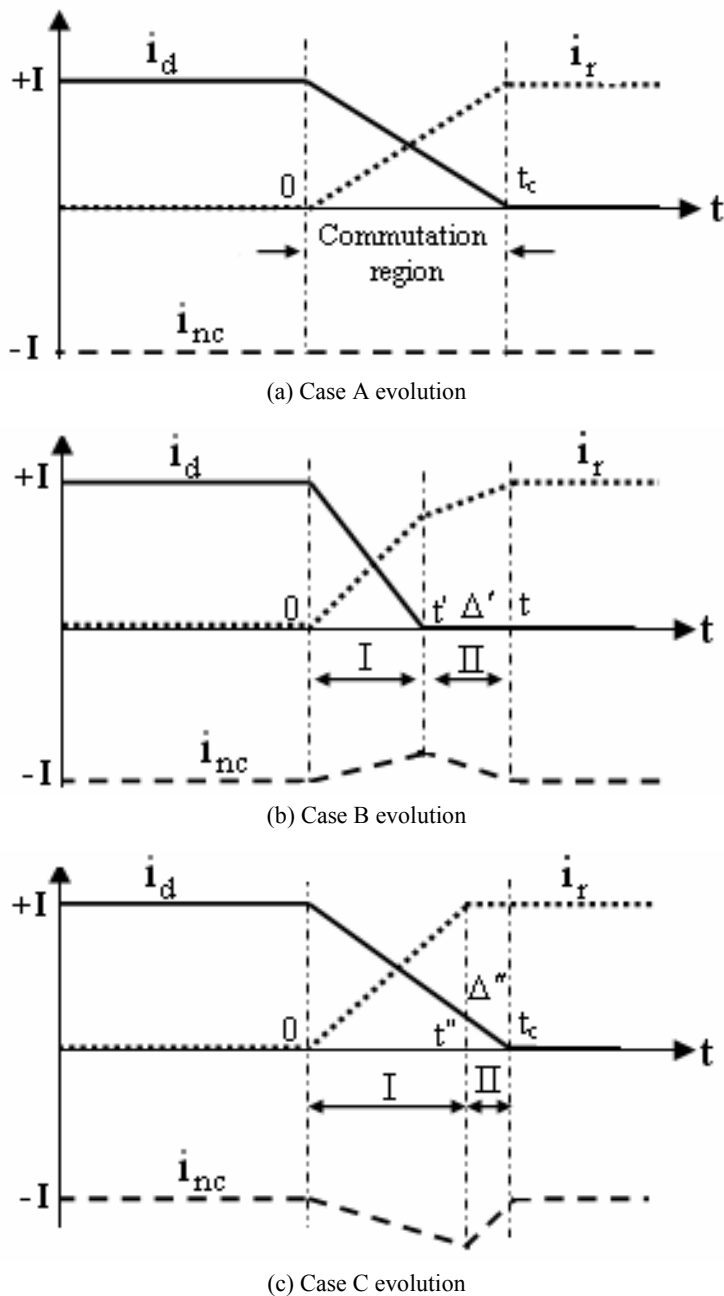


Fig. 3 Current behaviors during commutation.

3.2 Current Commutation

In this section, the current commutations are analyzed at positive current level which are corresponding to three modes II, IV and VI. Commutation at negative level in modes I, III and V can be explained in the same manner.

• Mode II

In mode II, i_d , i_f and i_{nc} (non-commutated current) are corresponding to i_c , i_a and i_b respectively and commutation is from phase c to phase a as shown in Fig. 1. The current is transferred by switching on S_1 and S_4 . Nevertheless, the current is not transferred directly and it is done firstly by action of the freewheeling diodes. The circuit before commutation is shown in Fig. 4(a). Immediately after switching on S_1 , the circuit topology is shown in Fig. 4(b). From this point, three cases A, B or C may be found. To analyze of circuit in these cases, the current derivatives are firstly obtained as follow.

For sequence of Fig. 4(b), the KVL equations using Eq. (1) are written as:

$$\begin{cases} +\frac{V}{2} = R \cdot i_a + L \frac{di_a}{dt} + e_a + V_{no} \\ -\frac{V}{2} = R \cdot i_b + L \frac{di_b}{dt} + e_b + V_{no} \\ 0 = R \cdot i_c + L \frac{di_c}{dt} + e_c + V_{no} \end{cases} \quad (3)$$

where, $L = L_s - M$. Substituting the phase back-EMF voltages with their magnitude, results:

$$V_{no} = -\frac{e_a + e_b + e_c}{3} = -\frac{E}{3} \quad (4)$$

With simplifications, current derivatives are expressed as:

$$\begin{cases} \frac{di_a}{dt} = \frac{3V - 4E}{6L} \\ \frac{di_b}{dt} = -\frac{3V - 8E}{6L} \\ \frac{di_c}{dt} = -\frac{4E}{6L} \end{cases} \quad (5)$$

With the same manner, for sequence of Fig. 4(c), current derivatives are obtained from:

$$\begin{cases} \frac{di_a}{dt} = -\frac{3V + 4E}{6L} \\ \frac{di_b}{dt} = \frac{3V + 8E}{6L} \\ \frac{di_c}{dt} = -\frac{4E}{6L} \end{cases} \quad (6)$$

According to the motor speed, three following cases can be occurred:

Case A:

In this case, i_c and i_a reach to their final values simultaneously and therefore, their slopes are equal. The condition for occurring case A from Eq. (5) is given by:

$$\frac{E}{V} = \frac{3}{8} \quad (7)$$

Taking the beginning of the commutation as the time origin and using Eq. (5), i_a is obtained from:

$$i_a(t) = \frac{3V - 4E}{6L} t \quad (8)$$

Therefore, the commutation time t_c can be calculated from:

$$t_c = \frac{3LI}{2E} \quad (9)$$

Case B:

In this case, the commutation is done in two sequences I and II. In the first sequence ($0 < t < t'$), i_c vanishes at $t = t'$. It is characterized by the circuit of Fig. 4(b) and from Eq. (5), $i_c(t)$ is obtained from:

$$i_c(t) = I - \frac{4E}{6L} t \quad (10)$$

The duration of this step is obtained from Eq. (10) as:

$$t' = \frac{6LI}{4E} \quad (11)$$

At the end of this sequence, from Eq. (6) i_a is given by:

$$i_a(t') = \frac{3V - 4E}{4E} I \quad (12)$$

For second sequence ($t' < t < t_c$), two phases a and b are conducting in series ($i_a = -i_b$), and KVL is as:

$$V = L \frac{di_a}{dt} + e_a - L \frac{di_b}{dt} - e_b = 2L \frac{di_a}{dt} + 2E \quad (13)$$

Therefore, the current i_a is expressed by:

$$i_a(t) = \frac{3V - 4E}{4E} I + \frac{V - 2E}{2L} t \quad (14)$$

At the end of sequence II, i_a reaches to $+I$. So, the duration of this step (Δ') is obtained from:

$$\Delta' = \frac{-3V+8E}{V-2E} \cdot \frac{2LI}{4E} \quad (15)$$

The total duration of the commutation in case B is calculated from Eq. (11) and Eq. (15):

$$t_c = t' + \Delta' = \frac{LI}{V-2E} \quad (16)$$

The condition to have the case B can be obtained from relations $i_a(t') < +I$ and $0 < t_c$ as:

$$\frac{3}{8} < \frac{E}{V} < 0.5 \quad (17)$$

This equation explores the high speed range of FSTPI-BLDC motor in modes II and V. In this case, current i_b is not directly involved in the commutation, but does not remain constant because the slopes of currents i_c and i_a

are different. The point $E/V = 0.5$ is the limit point for controlling the current and at higher speeds, the current control is impossible.

Case C:

For this case, the commutation is also made in two sequence I and II as shown in Fig. 3(c). At the first sequence ($0 < t < t''$), current i_c vanishes and it is characterized by the circuit of Fig. 4(b). At the end of this sequence, $i_a(t)$ reaches to $+I$. Using $i_a(t)$ from Eq. (5), duration of sequence I is obtained from:

$$t'' = \frac{6LI}{3V-4E} \quad (18)$$

Second sequence ($t'' < t < t_c$) corresponds to Fig. 4(c) and at the end of sequence II, i_c reaches to zero. Therefore, the duration of this step (Δ'') is:

$$\Delta'' = \frac{3V-8E}{3V-4E} \cdot \frac{6LI}{4E} \quad (19)$$

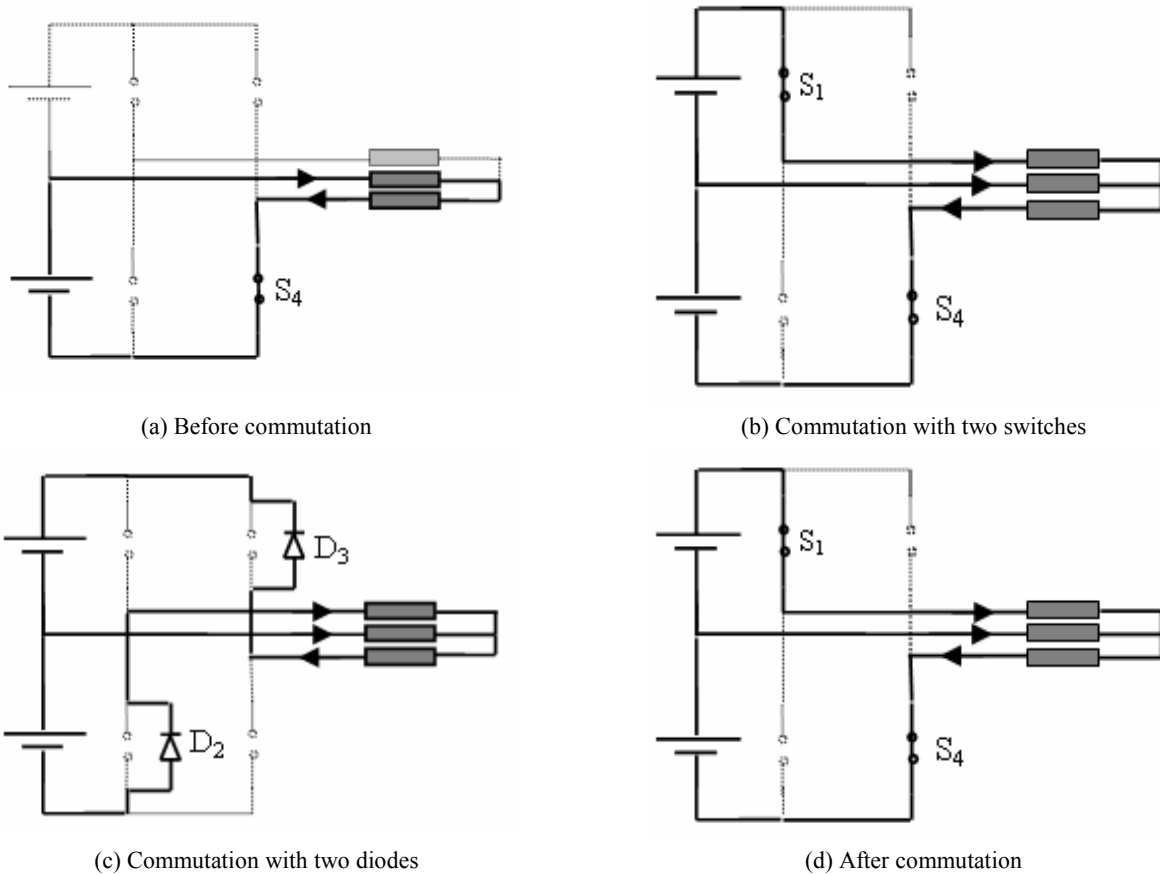


Fig. 4 Commutation sequence in mode II.

The total duration of the commutation in case C is the summation of Eq. (18) and Eq. (19) and is obtained from:

$$t_c = t'' + \Delta'' = \frac{3LI}{2E} \quad (20)$$

The condition to have the case C is given by $i_c(t'') > 0$, that results:

$$\frac{E}{V} < \frac{3}{8} \quad (21)$$

This equation explores the low speed range of FSTPI-BLDC motor in modes II and V. In this case, current i_b does not remain constant because the derivatives of currents i_c and i_a are different. Hence, in low speed range non-commutated current i_b has some swell as shown in Fig. 3(c) and therefore, the torque increases.

• **Mode IV**

In this operation mode, i_d , i_r and i_{nc} are corresponding to i_a , i_b and i_c respectively as shown in Fig. 1. According to the slopes of the currents i_a and i_b , it can be proved that in this mode always $|i_b| < |i_a|$ is valid and therefore, only case B occurs. The current derivatives are obtained from:

$$\begin{cases} \frac{di_a}{dt} = -\frac{3V+4E}{6L} \\ \frac{di_b}{dt} = -\frac{-3V+4E}{6L} \end{cases} \quad (22)$$

With the same manner of case B in mode II, the following equations are derived:

$$t' = \frac{6LI}{3V+4E} \quad (23)$$

$$\Delta' = \frac{8E}{3V+4E} \cdot \frac{4LI}{V-4E} \quad (24)$$

$$t_c = t' + \Delta' = \frac{2LI}{V-4E} \quad (25)$$

The condition to have this case is expressed as:

$$\frac{E}{V} < 0.25 \quad (26)$$

• **Mode VI**

In this operation mode, i_d , i_r and i_{nc} are corresponding to i_b , i_c and i_a respectively. Commutation is from phase b to phase c and the current is transferred by switching off S_3 and the switch S_2 is held on. With the same manner as mode II, three following cases can occur:

Case A:

$$t_c = \frac{6LI}{3V-4E} \quad (27)$$

The condition for this case is:

$$\frac{E}{V} = \frac{1}{8} \quad (28)$$

Case B:

$$t_c = t' + \Delta' = \frac{2LI}{V-4E} \quad (29)$$

The condition for case B that results from $i_c(t') < +I$ and $0 < t_c$ is obtained from:

$$\frac{1}{8} < \frac{E}{V} < 0.25 \quad (30)$$

Similar to the modes II and IV, point $E/V = 0.25$ is the limit point for controlling the current at mode VI and at higher speed, the current control is impossible.

Case C:

$$t_c = t'' + \Delta'' = \frac{6LI}{2V-4E} \cdot \frac{4V-4E}{3V+4E} \quad (31)$$

The condition to have the case C is:

$$\frac{E}{V} < \frac{1}{8} \quad (32)$$

In this section, the phase commutation in modes II, IV and VI have been analyzed. Developed equations show that the current commutations of six modes are different together and also are different to the commutation of six-switch inverter BLDC motor drive [4].

4. Torque Ripple at Commutations

General expression of torque for each operation mode is given by:

$$T = \frac{2EI}{\omega} = T_n \quad (33)$$

Also, the torque between commutations is given by:

$$T = \frac{1}{\omega} (e_d i_d + e_r i_r + e_{nc} i_{nc}) \quad (34)$$

Supposing the back-EMF voltages are constant in the commutation result in:

$$T = \frac{1}{\omega} (E \cdot i_d + E \cdot i_r - E \cdot i_{nc}) = -\frac{2E \cdot i_{nc}}{\omega} \quad (35)$$

To calculate the torque ripple during commutation, the current value of the non-commutated phase should be obtained at the end of the corresponding case.

• Mode II

According to Fig. 4(b) and using Eq. (5) the current of the non-commutated phase is expressed as:

$$i_{nc}(t) = i_b(t) = -I - \frac{3V - 8E}{6L} t \quad (36)$$

Case A:

In this case, $i_b(t)$ is constant in commutation and then, torque remains constant and is equal to T_n . Therefore,

$$\Delta T = 0 \text{ (pu)} \quad (37)$$

Case B:

By substituting t' from Eq. (11) into Eq. (34), the torque at the end of the first step is obtained from:

$$T(t') = \frac{2EI}{\omega} \left[1 + \frac{3V - 8E}{4E} \right] \quad (38)$$

With some calculations and dividing by T_n , the relative torque ripple in per unit is given by:

$$\Delta T = \frac{T(t') - T_n}{T_n} = \frac{3V - 8E}{4E} \text{ (pu)} \quad (39)$$

Case C:

By substituting t'' from Eq. (18) into Eq. (34), the torque at the end of the first step is obtained from:

$$T(t'') = \frac{2EI}{\omega} \left[1 + \frac{3V - 8E}{3V - 4E} \right] \quad (40)$$

Therefore, the relative torque ripple is given by:

$$\Delta T = \frac{T(t'') - T_n}{T_n} = \frac{3V - 8E}{3V - 4E} \text{ (pu)} \quad (41)$$

With the same manner for mode IV and mode VI, the results are as the following:

• Mode IV

$$\Delta T = \frac{T(t') - T_n}{T_n} = \frac{-8E}{3V + 4E} \text{ (pu)} \quad (42)$$

• Mode VI

Case A:

$$\Delta T = 0 \text{ (pu)} \quad (43)$$

Case B:

$$\Delta T = \frac{T(t') - T_n}{T_n} = \frac{V - 8E}{V + 4E} \text{ (pu)} \quad (44)$$

Case C:

$$\Delta T = \frac{T(t'') - T_n}{T_n} = \frac{V - 8E}{2V - 4E} \text{ (pu)} \quad (45)$$

The same explanations can be presented for modes I, III and V that the same results for commutation torque ripple are obtained. Equations (37), (39) and (41)-(45) are only valid if the current can be controlled. It means they must not exceed their corresponding limit points. For a typical BLDC motor, the commutation duration and the relative torque ripple calculated as equations (36)-(45) are shown in Fig. 5 and Fig. 6 respectively. For six-switch inverter topology, commutation duration has an optimum value at point $V = 4E$ and also the current limit point is $V = 2E$. However, in the four switch topology, commutation duration for all modes is increasing when the speed increases and has not any optimum value. Moreover, the current limit point reduces to point $V = 4E$. This is the main drawback of four-switch topology. It means that for $E/V > 1/4$ region, the current control is impossible and actually the

drive operation range is limited to $E/V < 1/4$ conditions and half of the nominal speed range of BLDC motor drive is accessible.

On the other hand, the torque ripple equations show that for a constant voltage of DC-Bus, the relative torque ripple due to commutation is independent of the current, but it depends on the back-EMF voltage (i.e. speed). Because of asymmetrical stator phase voltages, the torque ripple are different at each operation mode as shown in Fig. 6. The relative torque ripple reaches to 50% and 100% at low speed in different modes and it reduces to -50% at $V = 4E$. In the next section, for commutation torque ripple reduction at $E/V < 1/4$ region, a novel control technique is presented.

5 Commutation Torque Ripple Reduction

Commutation analysis of four-switch inverter shows that the commutation ripple always occur and torque ripple reduction is necessary. Using the direct phase current (DPC) control technique at commutation duration can eliminate the commutation torque ripple in low speed range (case C) in a FSTPI-BLDC motor drive. However, for high speed range (case B) it has been shown that DPC control method can not eliminate the commutation torque ripple [8]. Therefore, a novel control technique based on the current slope equalization of two commutated currents is developed. This technique eliminates the torque ripple at wide range lower than current limit point $V = 4E$. Proposed technique is developed for modes II, IV and VI and for other modes, the developed relations are the same.

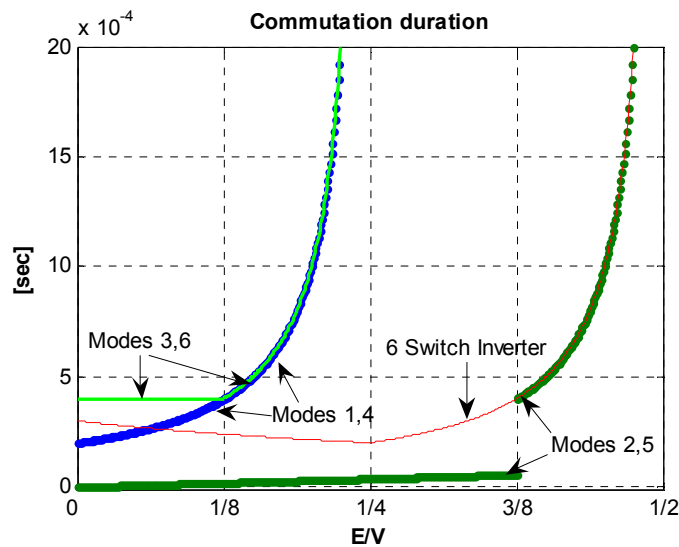


Fig. 5 Commutation duration in different modes.

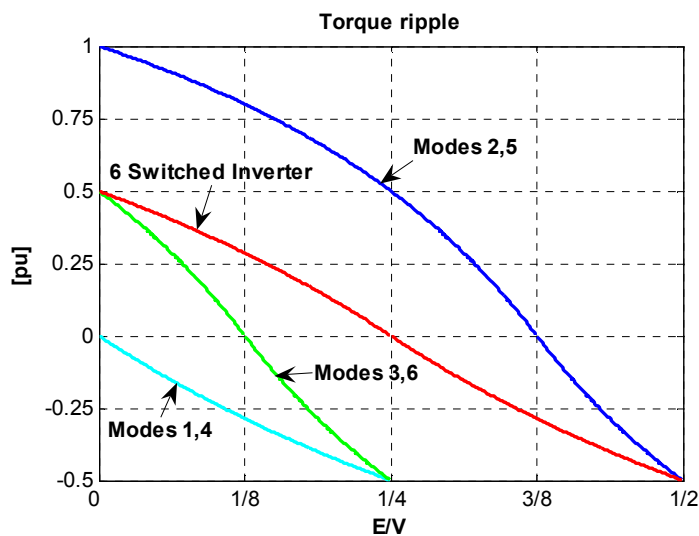


Fig. 6 Relative torque ripple amplitude in different modes.

5.1 Torque Ripple Reduction in Mode II

Case B:

In this case the current limit point is $E/V = 0.5$ and so for interval $3/8 < E/V < 0.5$, the current control or torque ripple reduction can be used. However the region $3/8 < E/V < 0.5$ is above of the current limit point for modes I, III, IV, VI that is $V = 4E$. It means the current control in this case of two modes II and V is useless.

Case C:

In this case, it is clear that $|i_c| < |i_a|$, as shown in Fig. 7(a) and from Eq. (6). To match the slopes of the currents, switching on S_4 decreases the slope of i_a and slope equalization can be obtained. Solid line shows the modulated waveform of the current i_a . Fig. 7(b) shows the circuit topology when S_1 is off. KVL equations during commutation intervals can be written as:

$$\begin{cases} S \cdot \frac{V}{2} = R \cdot i_a + L \frac{di_a}{dt} + e_a + V_{no} \\ -\frac{V}{2} = R \cdot i_b + L \frac{di_b}{dt} + e_b + V_{no} \\ 0 = R \cdot i_c + L \frac{di_c}{dt} + e_c + V_{no} \end{cases} \quad (46)$$

and the voltage of the motor star point is given by:

$$V_{no} = (S-1) \cdot \frac{V}{6} - \frac{E}{3} \quad (47)$$

that, $S = +1$ refers to S_1 is on during the switching and $S = -1$ refers to S_1 is off. The averaged slopes of commutated currents are obtained as the following:

$$\begin{aligned} \frac{d\bar{i}_a}{dt} &= \frac{1}{6L} [4D \cdot V - V - 4E] \\ \frac{d\bar{i}_c}{dt} &= \frac{1}{6L} [-2D \cdot V + 2V - 4E] \end{aligned} \quad (48)$$

Equalizing slopes of currents i_a and i_c leads to:

$$D_{2,Low-1} = -\frac{1}{2} + \frac{4E}{V} \quad (49)$$

From Eq. (21), $0 < E/V < 3/8$. But, $D_{2,Low}$ will be between 0 and 1 if $1/8 < E/V < 3/8$. Moreover, it can be shown that for interval $0 < E/V < 1/8$, by switching on S_4 , slope matching between currents i_a and i_c is accessible. Therefore, in this case the duty cycle is obtained from:

$$D_{2,Low-2} = \frac{3}{4} + \frac{2E}{V} \quad (50)$$

Torque Ripple Reduction in Mode IV

Section 3.2 stated that in mode IV only case B occurs. Equation (22) shows $|i_b| < |i_a|$. Matching of these current slopes can be obtained either by decreasing the slope of i_a via switching of S_1 . Fig. 8 shows the circuit topology and current behavior in this case. With the same manner, the averaged slopes of commutated currents are obtained:

$$\begin{aligned} \frac{d\bar{i}_a}{dt} &= \frac{1}{6L} [4D \cdot V - 3V - 4E] \\ \frac{d\bar{i}_b}{dt} &= \frac{1}{6L} [-2D \cdot V + 3V - 4E] \end{aligned} \quad (51)$$

Equalizing the slopes of the currents i_a and i_b results:

$$D_{4,High} = \frac{4E}{V} \quad (52)$$

Equation (26) presents that the current control limit point in modes I and IV is $E/V = 0.25$. Therefore, the developed duty cycle $D_{4,High}$ is valid.

Torque Ripple Reduction in Mode VI

Case B:

From Eq. (30), the current control for interval $1/8 < E/V < 0.25$ is possible. Fig. 9 shows the circuit topology and current behavior in this case and it can be shown that $|i_c| < |i_b|$. In order to match the current slopes, switching on S_3 decreases the slope of i_b . Using the same explanation, duty cycle is obtained from:

$$D_{6,High} = \frac{4E}{V} - \frac{1}{2} \quad (53)$$

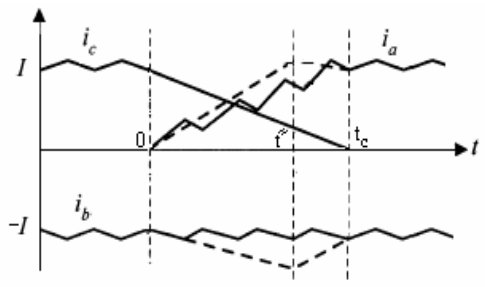
From Eq. (30), $1/8 < E/V < 0.25$ and therefore, the developed duty cycle is valid.

Case C:

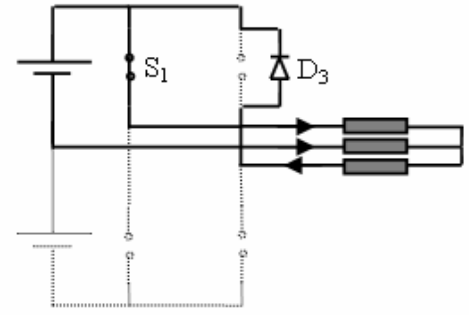
In this case, it can be shown that $|i_b| < |i_c|$. To match of these current slopes, switching on S_2 decreases the slope of i_c and therefore, the slopes become equal. Fig. 10 shows the circuit topology and current behavior in this case. With the same manner, duty cycle is obtained from:

$$D_{6,Low} = \frac{3}{4} + \frac{2E}{V} \quad (54)$$

From Eq. (30), $E/V < 1/8$ and therefore, developed duty cycle is valid. Fig. 11 shows the developed duty cycles of new torque ripple reduction technique for all operation modes. Moreover, the used switches at each mode have been indicated. As long as the speed increases the duty cycle of the corresponding switches increases. Actually the duty cycles are valid at lower than point $V = 4E$.

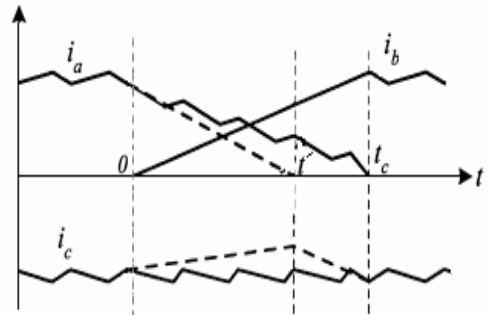


(a) Current behavior

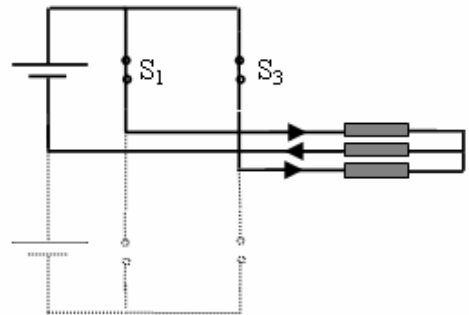


(b) Circuit topology when S_1 is on

Fig. 7 Current behavior and circuit topology in case C (low speed) of mode II.

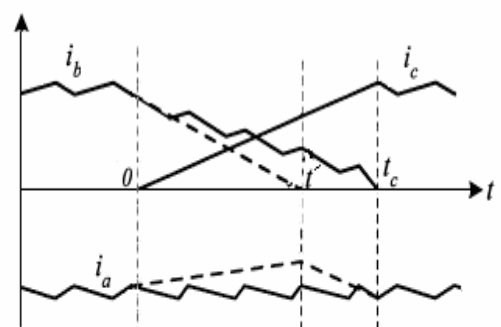


(a) Current behavior

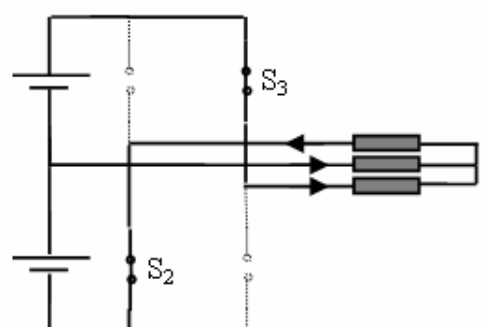


(b) Circuit topology when S_1 is on

Fig. 8 Current behavior and circuit topology in mode IV.

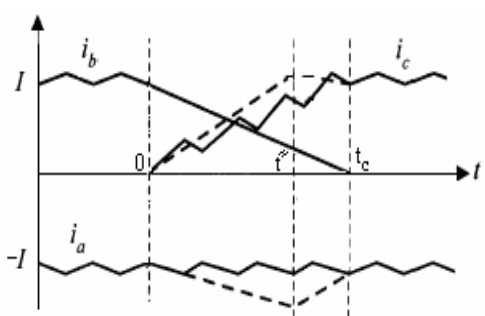


(a) Current behavior

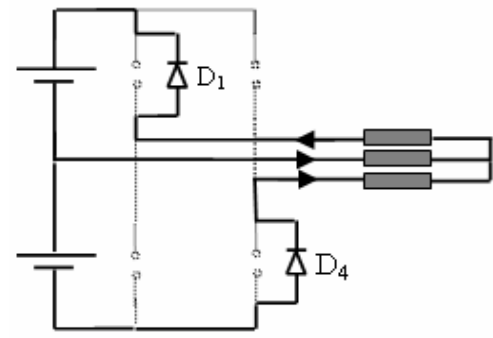


(b) Circuit topology when S_3 is on

Fig. 9 Current behavior and circuit topology in case B (high speed) of mode VI.



(a) Current behavior



(b) Circuit topology when S_2 is off

Fig. 10 Current behavior and circuit topology in case C (low speed) of mode VI.

6 Simulation

This section verifies the developed torque ripple reduction technique via simulation. Fig. 12 shows the block diagram of the implemented four-switch brushless DC motor drive system in Simulink. Speed control block creates the current reference. In current control block using direct phase current (DPC) control method, the phase currents are regulated and duty cycles (D_a and D_b) of the power switches are developed.

In torque ripple reduction block, developed duty cycles, for commutation intervals are modified using the new proposed duty cycles in this paper which are shown in Fig. 11. In power inverter block, proper phase voltages are generated and applied to BLDC motor. In BLDC motor block, phase currents are calculated from Eq. (1). The rated parameters of BLDC motor are given in the table 1.

The performance of the system is simulated at speed 2000 rpm. Fig. 13 shows the current waveforms in both cases with and without applying the proposed torque ripple reduction technique. Current ripple reaches to 2.5 [A] (about 40%) that is high. Using the developed technique it reduces to lower than 7%.

Fig. 14 shows the electromagnetic torque without and with applying the proposed technique. In simulation, DC-link voltage of the inverter has set to 160 [V] and at speed $\omega=2000$ rpm, back-EMF voltage becomes 22.4 [V] and so, E/V ratio is between 1/8 and 3/8. Fig. 15 shows the current commutation in mode II (IV) without using the developed torque ripple reduction method. It

is obvious that there is not any commutation torque ripple. It is because of that, $\omega=2000$ rpm is located at low speed range of mode II and as stated earlier, DPC control method can eliminate commutation torque ripple in case C. However, for modes IV (I) and VI (III) as stated in Eq. (26) and Eq. (30), operation point $\omega=2000$ rpm is located at high speed range (case B) of these modes and it is prospected that commutation torque ripple occurs.

Fig. 16 shows the commutation ripple reduction in mode IV. Using developed technique significantly reduces the current ripple and current slopes become equal together. Fig. 17 shows the switching command to switch S_1 that is set to 56%. DPC controller commands switch S_1 to be zero, but developed torque ripple reduction method, during commutation modifies it to 56%. Fig. 18 shows the current waveforms in mode VI. Applying the proposed technique reduces the current ripple significantly.

Table 1 Rated Parameters of BLDC Motor.

P_{rated}	1 [hp]	ω_{rated}	3500 [RPM]
R	0.75 [Ω]	L_{s-M}	3.05 [mH]
K_t	0.21 [N.m/A]	K_e	0.107 [V/(rad/sec)]
I_{rated}	10 [A]	V_{DC-Bus}	160 [V]
Z_p	2	J	8.2e-5 [Kg m^2]

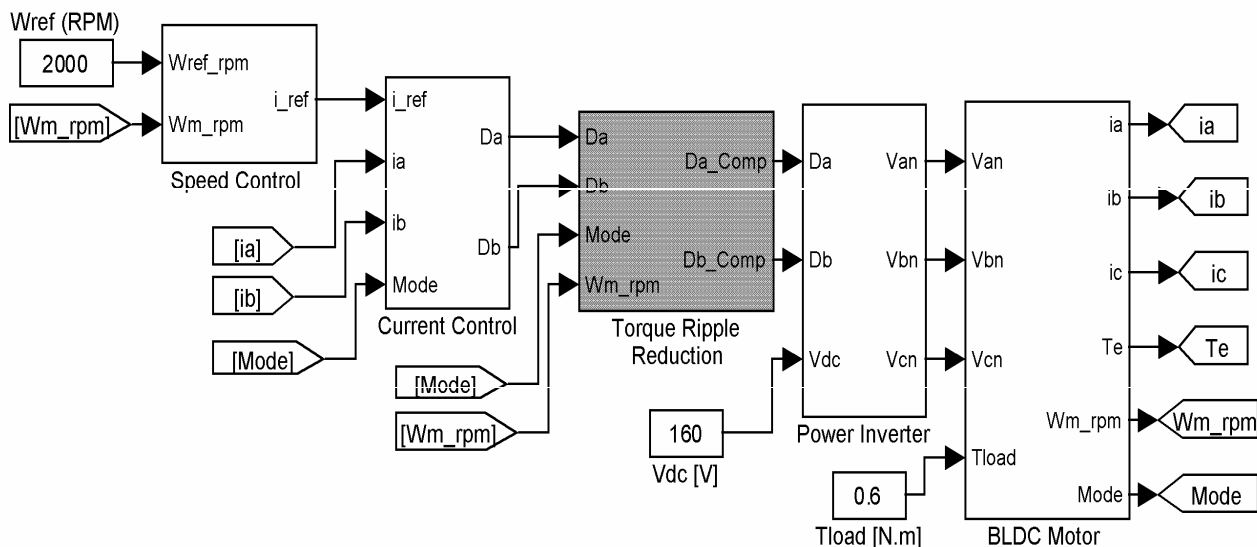
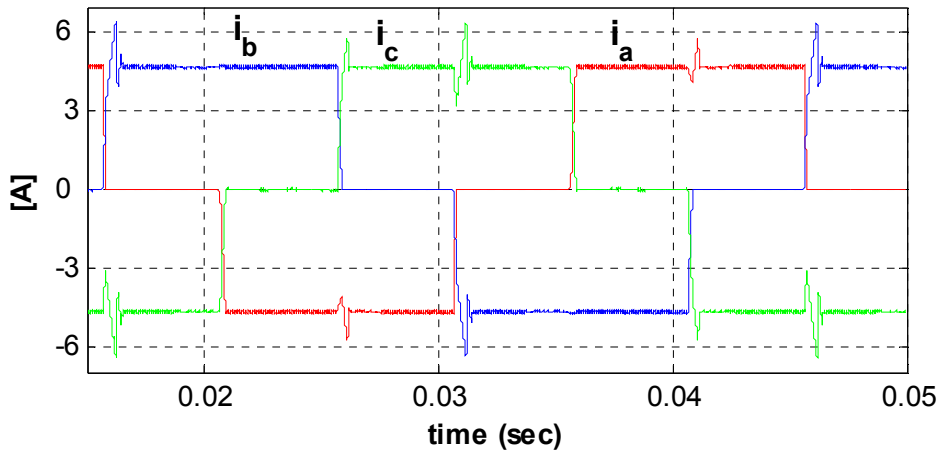
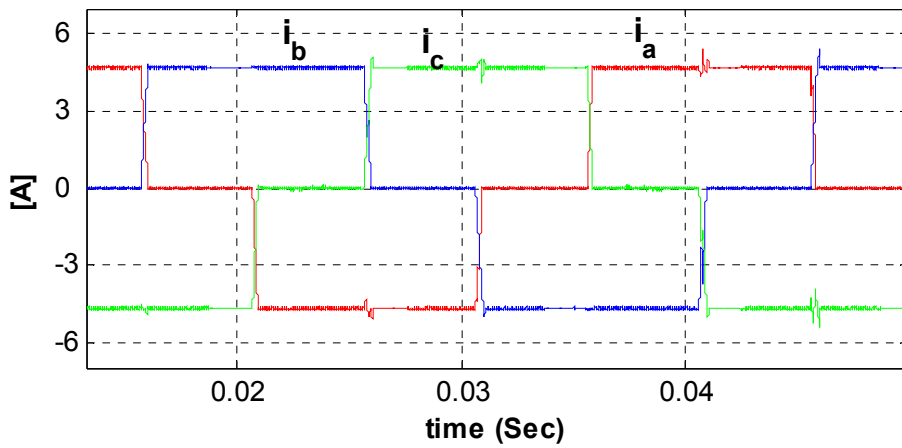


Fig. 12 Block diagram of the FSTPI-BLDC motor drive with new commutation torque ripple reduction technique.



(a) Without applying the torque ripple reduction



(b) With applying the torque ripple reduction

Fig. 13 Phase current waveforms of the BLDC motor with/without torque ripple reduction

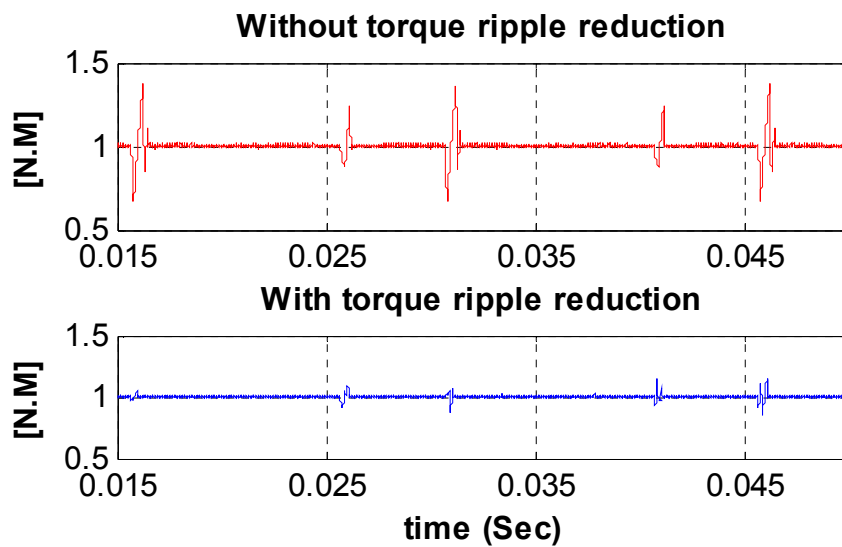


Fig. 14 Developed electromagnetic torque in different modes.

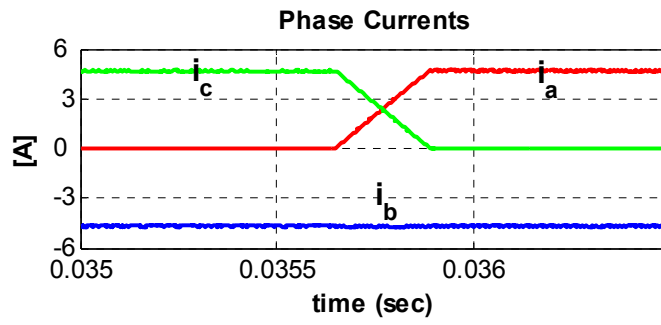


Fig. 15 Current commutation in mode II without using torque ripple reduction.

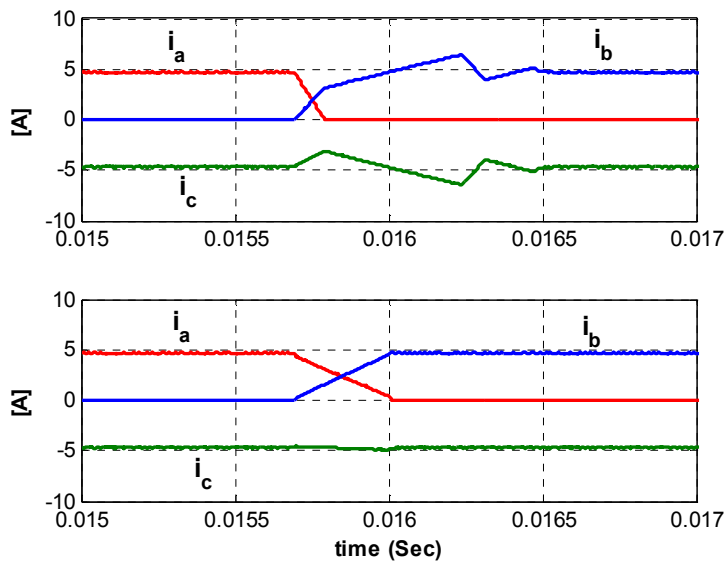


Fig. 16 Current commutation in mode IV.

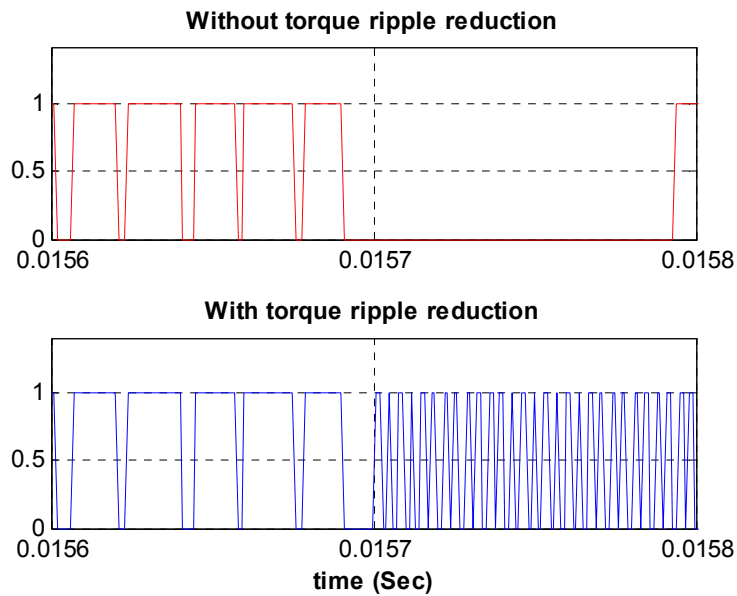


Fig. 17 Applied switching command to S_1 in mode IV.

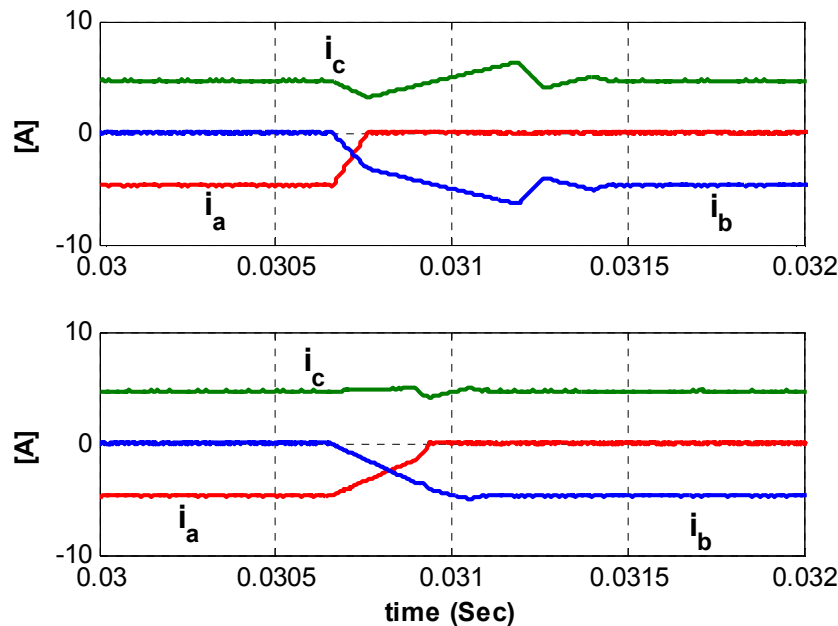


Fig. 18 Current commutation in mode VI.

7 Conclusion

This paper presents an analytical study on commutation torque ripple in a four-switch brushless DC motor drive. The analytic equations of commutation intervals and commutation torque ripple are obtained for each mode. Developed results show that phase commutation characteristics are different for each operation modes. It shows also the limit point of current control reduced to half of its corresponding point in a six-switch inverter. It means the accessible speed range that the phase currents can be regulated as rectangular is limited to half of nominal speed. Then, a new torque ripple reduction technique has been developed that is capable to eliminate the torque in entire speed range lower than current control limit point $V = 4E$. Actually it is based on the slope equalization of the decaying and rising current in commutation interval. The validity of the developed method has been demonstrated via simulation. The proposed method enhances the performance of the FSTPI-BLDC motor drive and can be used for low cost applications.

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